

FIG.1

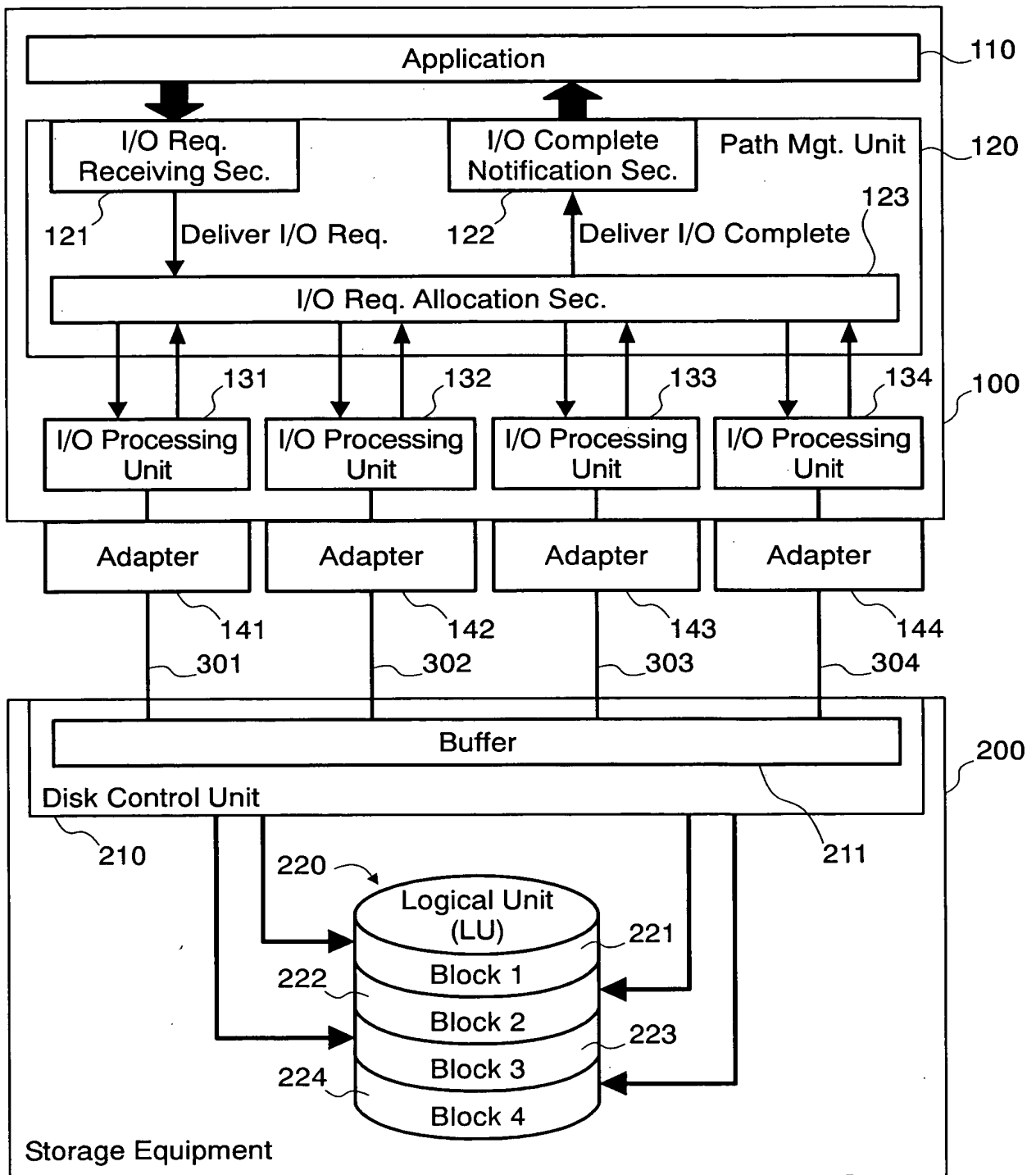


FIG.2

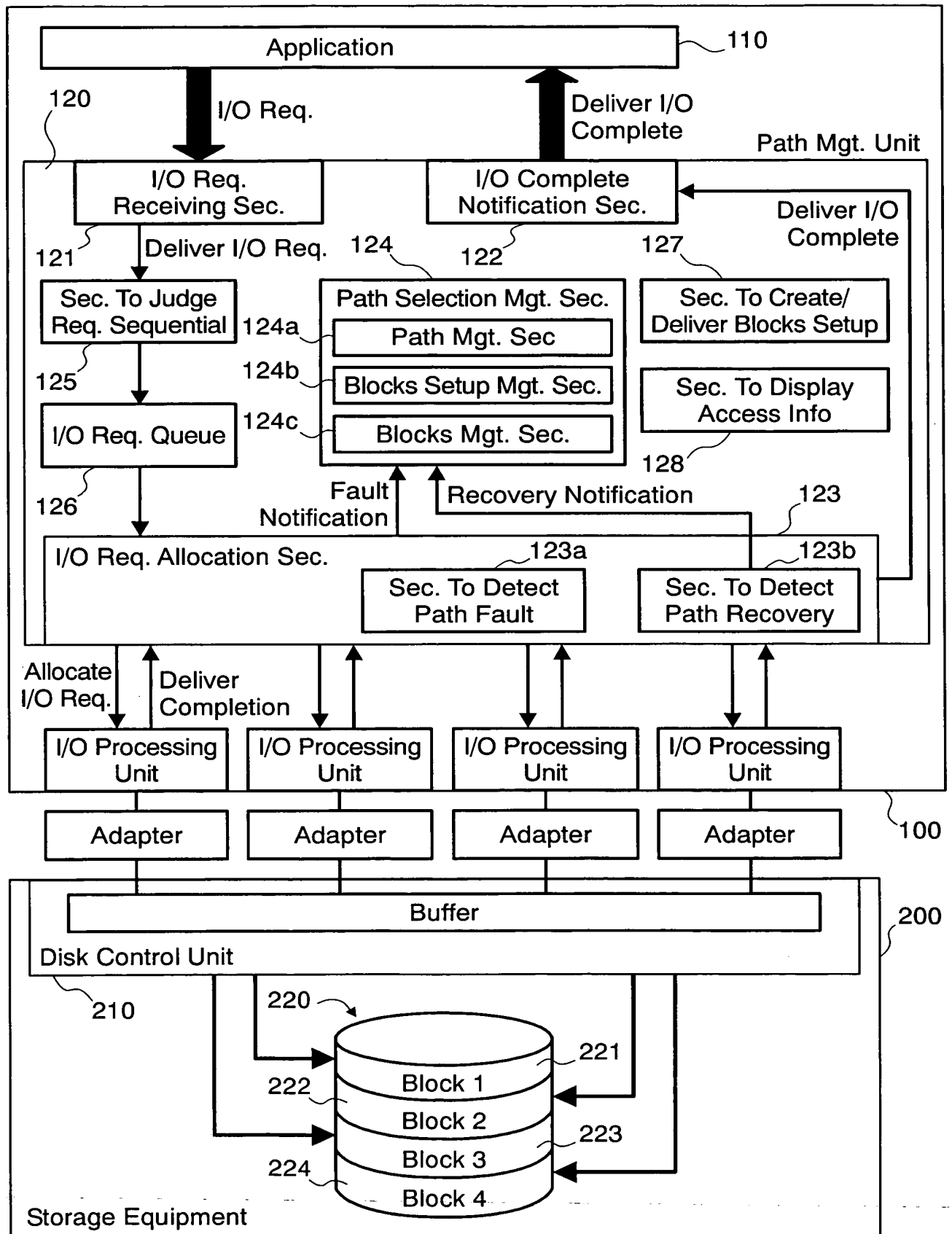


FIG.3

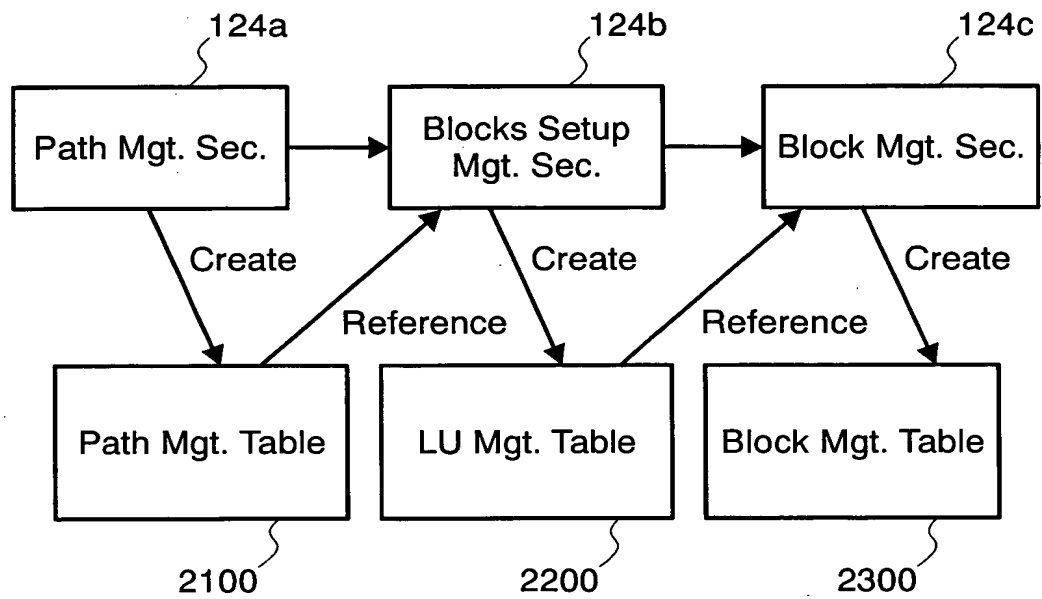


FIG.4

2101	2102	2103	2104	2105
Path ID	LU	Path Status	Sequential I/O Count	Random I/O Count

Path Management Table

2100

FIG.5

2201	2202	2203	2204
LU	LU Size	Number of Paths	Path IDs

LU Management Table

2200

FIG.6

2301	2302	2303	2304	2305	2306	2307
Block ID	Block Length	LU	Start Address	Path ID	Sequential Access Count	Random Access Count
1						
2						

Block Management Table 2300

FIG.7

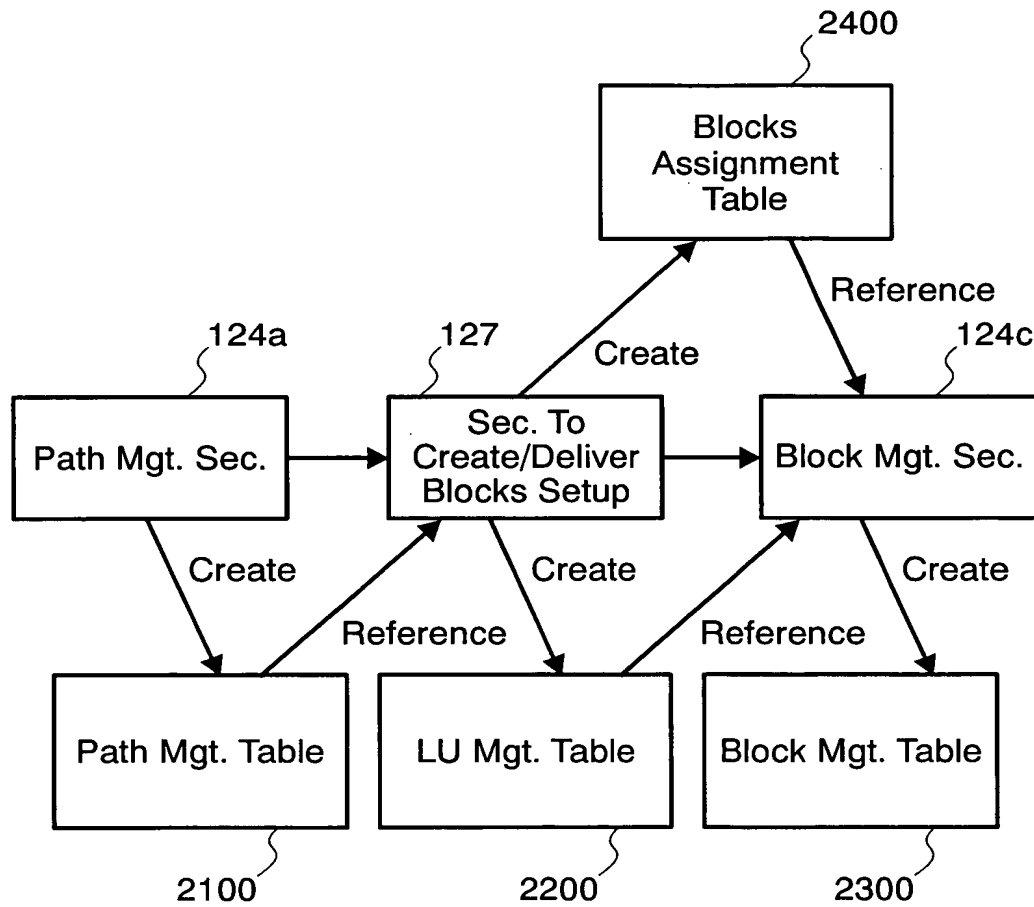


FIG.8

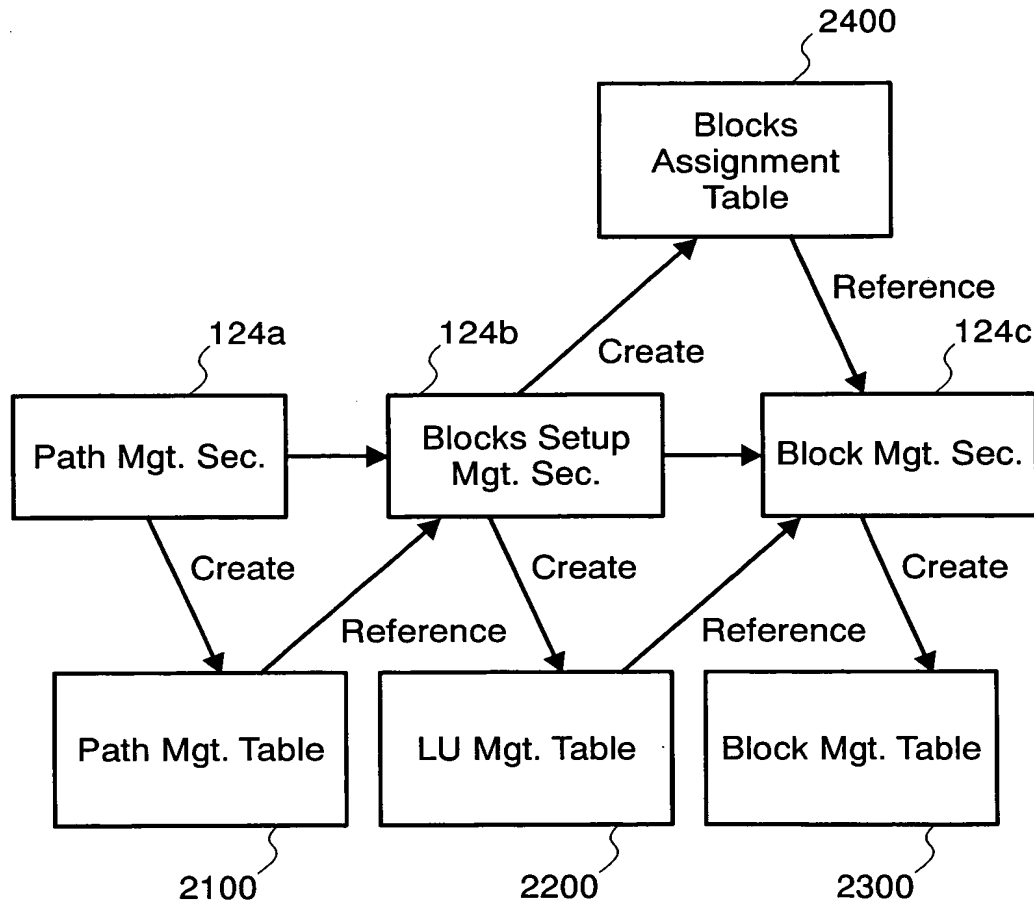


FIG.9

2401 LU	2402 Number of Active Paths	2403 Start Address	2404 Number of Assigned Paths
1	4	1,000	2
		3,000	1
		6,000	1
	3	1,000	2
		3,000	1
	2	1,000	1
		3,000	1
	1	1,000	1
2	4	1,000	4

Blocks Assignment Table

2400

FIG.10

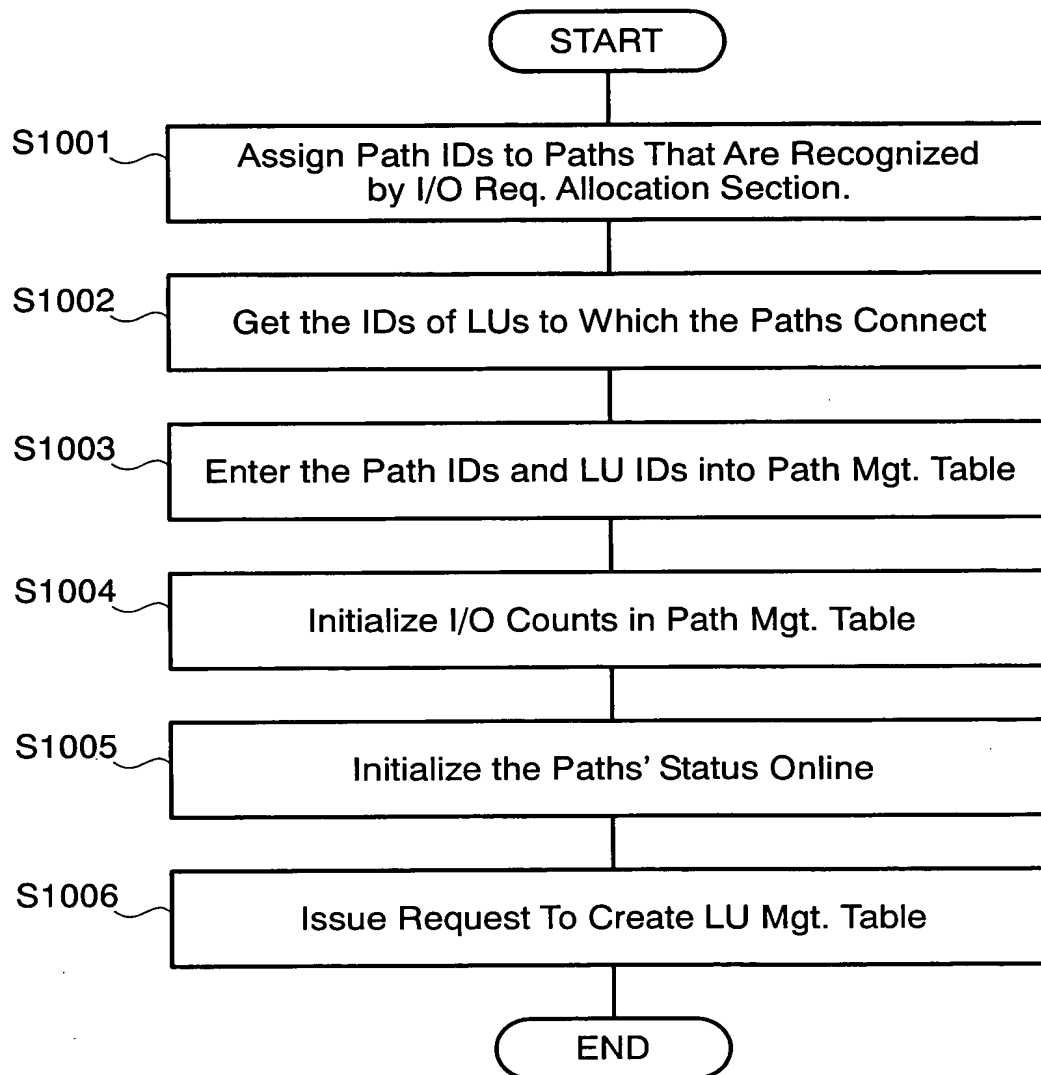


FIG.11

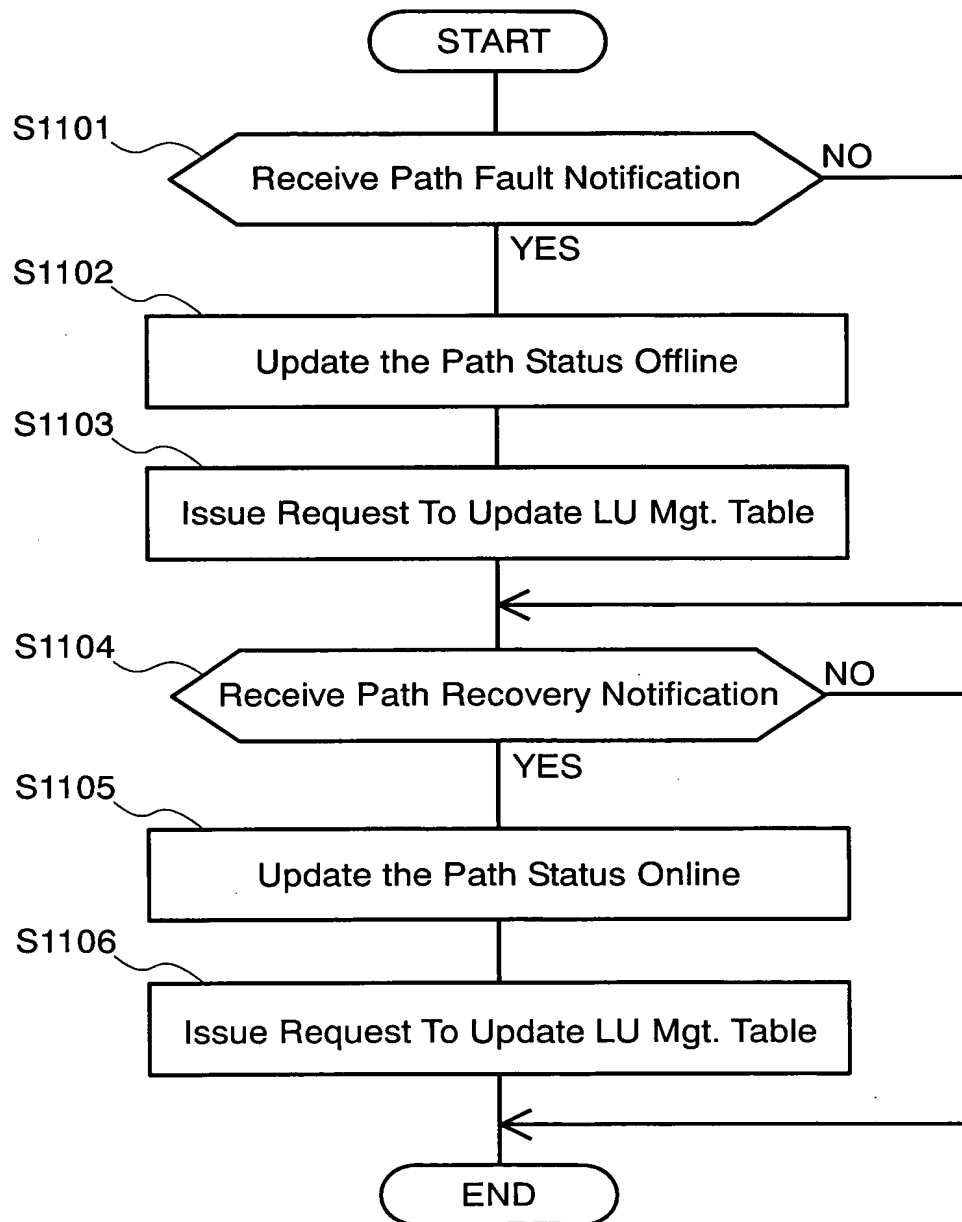


FIG.12

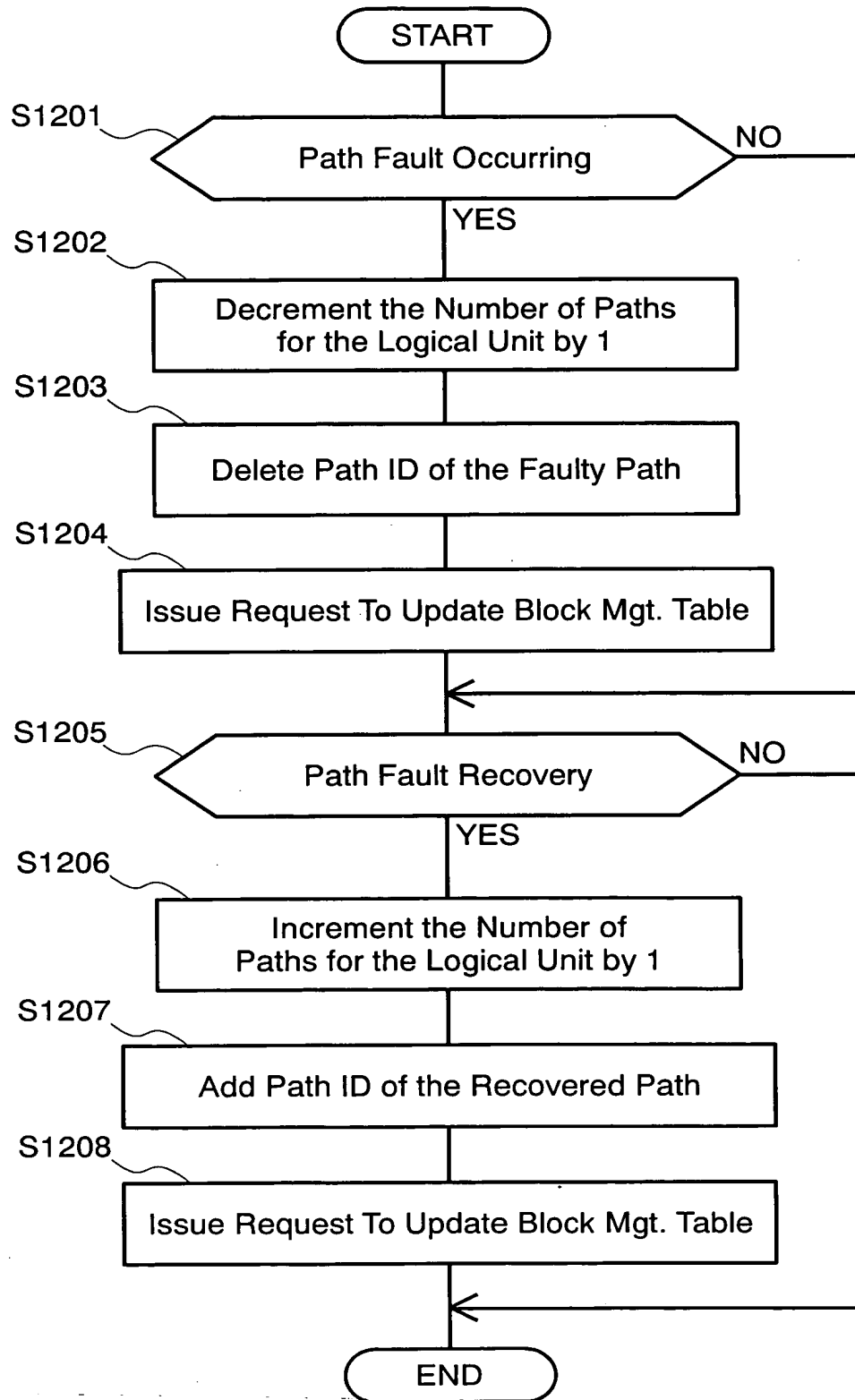


FIG.13

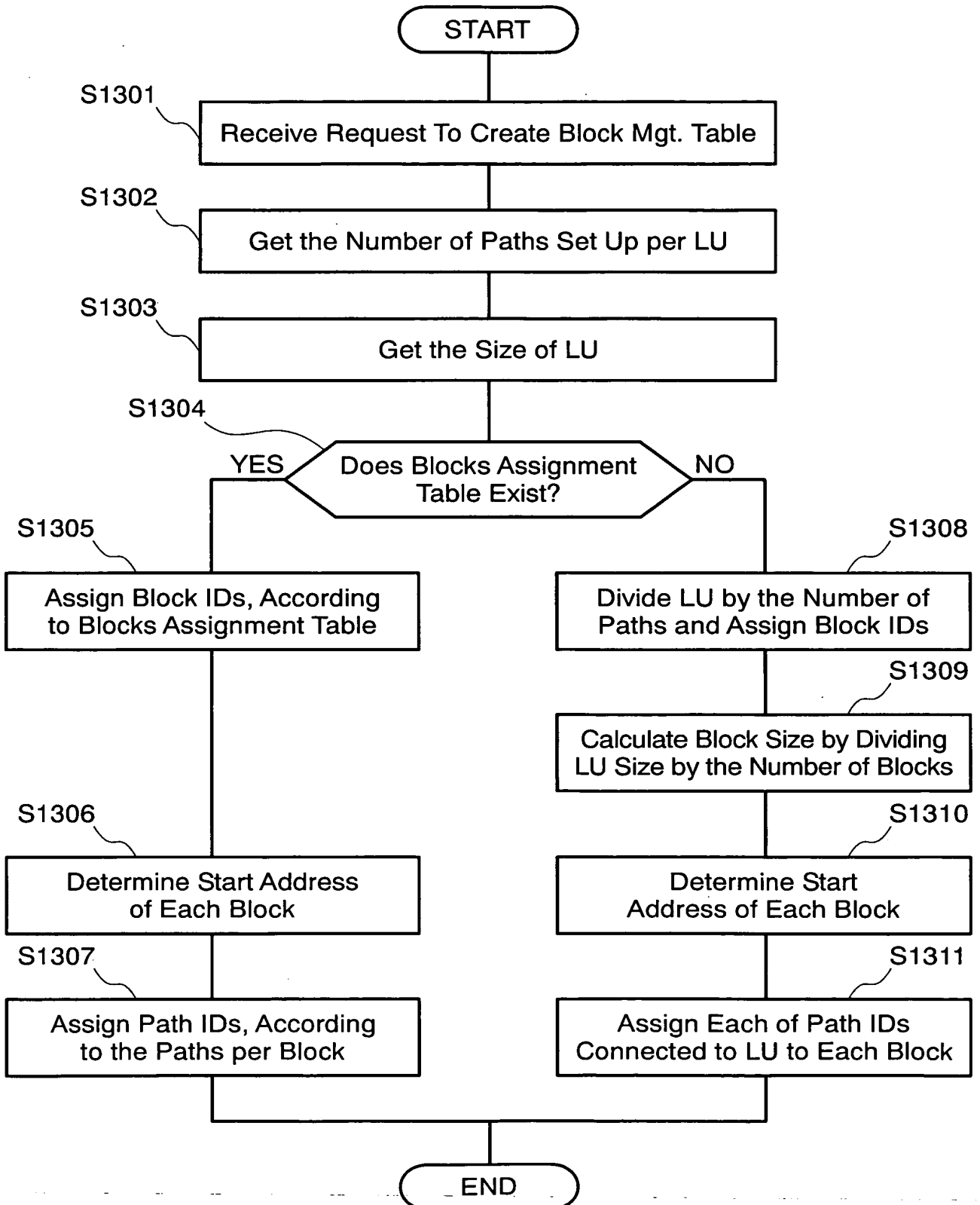


FIG.14

